

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	13656	(multi adj1 chip adj1 module) or MCM	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 12:57
S2	2496	(advanced adj instrument adj controller) or AIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 12:57
S3	16274	(field adj programmable adj gate adj array) or FPGA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:26
S4	23	S1 near5 S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:15
S5	35	S1 with S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:08
S6	2	S2 near5 S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:09
S7	4	S2 with S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:09
S8	3	("4887239"   "5365591"   "5369314").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/19 13:10
S9	1	("6081896").URPN.	USPAT	OR	OFF	2004/11/19 13:11
S10	6	("5563928"   "5678057"   "5761516"   "5973376"   "6049748"   "6061260").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/11/19 13:11
S11	165834	(parallel or (in adj parallel) or simultaneous\$2 or concurrent\$2) near3 (process\$4 or execut\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:06

S12	34541	S11 with (processor\$1 or CPU\$1 or microprocessor\$1 or coprocessor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:21
S13	62	S12 with S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:22
S14	1	S13 same S1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:22
S15	1	S13 same S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:22
S16	169387	microcomputer or (micro adj computer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:37
S17	202	S16 near5 S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:50
S18	3	S17 same S1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:45
S20	264	S16 with S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:59
S21	0	S20 same S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 13:52
S22	122	S11 with S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 14:00

S23	38054	(application adj specific adj integrated adj circuit) or ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 14:15
S24	5256	S23 with S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 14:17
S25	3	S24 with S12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 14:19
S26	5	S24 with S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 14:19
S27	9	S2 with S23	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 14:45
S28	3212	independent\$2 near3 S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 14:50
S29	0	idependent\$2 adj3 S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 14:52
S30	13992	independent\$2 near2 (run\$4 or execut\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:06
S31	244	S30 with ((parallel or (in adj parallel) or simultaneous\$2 or concurrent\$2) near3 (process\$2 or task\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 14:57
S32	3	S30 with S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:02

S33	310716	(run\$4 or execut\$3) near3 (process\$3 or task\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:06
S34	3275	independent\$2 near2 S33	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:24
S35	527	S34 with (parallel or (in adj parallel) or simultaneous\$2 or concurrent\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:09
S36	2	S35 same S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:10
S37	48094	implement\$3 near3 (processor\$1 or microprocessor\$1 or CPU\$1 or coprocessor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:21
S38	440	S37 with S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:18
S39	237	S38 with S23	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:20
S40	0	S39 with parallel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:21
S41	347	implement\$3 near3 coprocessor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:22
S42	9	S41 with S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:22

S43	7	S34 with coprocessor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:24
S44	2652	implement\$3 near4 ((field adj programmable adj gate adj array) or FPGA)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:26
S45	15	S44 with coprocess\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/19 15:27